

-1-

UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	:	Group Art Unit:
Roger D. Weekly	:	Not Yet Assigned
	:	
Serial No.:	:	
Not Yet Assigned	:	
	:	
Filed:	:	IBM Corporation
(herewith)	:	Intellectual Property Law
	:	11400 Burnet Road
Title: TRIANGULAR ASSIGNMENT OF	:	Internal Zip 4054
PINS USED FOR DIAGONAL	:	
INTERCONNECTIONS BETWEEN	:	
DIAGONAL CHIPS IN A MULTI-CHIP	:	Austin, Texas 78701
MODULE	:	

J1017 U.S. PTO
10/099776
03/14/02INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

This Information Disclosure Statement is being submitted in connection with the above-identified application for patent. Applicant submits herewith patents, publications or other information of which he is aware, which he believes may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. § 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 C.F.R. § 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 C.F.R. § 1.56(a) exists.

The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 C.F.R. § 1.98(a)(1).

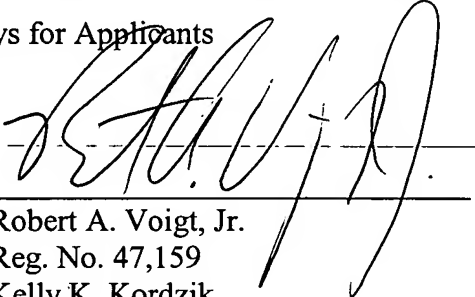
A copy of each of the items identified on the attached Form PTO-1449 is supplied herewith, except for the pending patent applications, for which no copies are being submitted.

Respectfully submitted,

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